

FPGA 2005: Call for Papers

13th ACM International Symposium on Field-Programmable Gate Arrays

**Hyatt Regency Hotel
Monterey, California
February 20-22, 2005**

The ACM/SIGDA International Symposium on Field-Programmable Gate Arrays is the premier conference for presentation of advances in all areas related to FPGA technology. For FPGA 2005, we are soliciting submissions describing novel research and developments in the following (and related) areas of interest:

- **FPGA Architecture:** Novel logic block and routing architectures, combination of FPGA fabric and system blocks (processors, etc.), new commercial architectures, impact of modern and future technologies (including ultra-deep submicron and nanometer scale) on the design of FPGA's (e.g. soft errors, leakage, power density, fabrication defects).
- **CAD for FPGAs:** Placement, routing, logic optimization, technology mapping, system-level partitioning, logic generators, testing and verification, CAD for FPGA-based accelerators, CAD for incremental FPGA design.
- **Circuit Design for FPGAs:** Novel FPGA circuits and circuit-level techniques.
- **FPGA-based and FPGA-like computing engines:** Compiled accelerators, reconfigurable computing, adaptive computing devices, systems and software.
- **Rapid-prototyping:** Fast prototyping for system-level design, Multi-Chip Modules (MCMs), logic emulation.
- **Applications:** Innovative use of FPGAs, exploitation of FPGA features, novel circuits, high-performance and low-power/mission-critical applications, DSP techniques, uses of reconfiguration, FPGA-based cores.

Authors are invited to submit English language PDF of their paper (12 pages maximum) and panel proposals by September 27, 2004 using the web interface at <http://www.isfpga.org>. Notification of acceptance will be sent by November 26, 2004. The authors of accepted papers will be required to submit the final camera-ready copy by December 6, 2004. A proceedings of the accepted papers will be published by ACM, and included in the Annual ACM/SIGDA CD-ROM Compendium publication;

Address questions to: Steve Wilton, Program Chair FPGA 2005
Department of Electrical and Computer Engineering
University of British Columbia
Vancouver, B.C., Canada V6T 1Z4
Phone : (604) 822-1263
Fax : (604) 822-5949
Email : stevew@ece.ubc.ca

Organizational Committee:

General Chair: Herman Schmit, Tabula

Program Chair: Steve Wilton, University of British Columbia

Finance Chair: Russell Tessier, University of Massachusetts at Amherst

Publicity Chair: Andre' DeHon, Caltech

Panel Chair: Michael Butts, Tabula

Program Committee:

Ray Andraka, Andraka Consulting Ltd

Trevor Bauer, Xilinx

Vaughn Betz, Altera

Mike Butts, Tabula

Katherine Compton, University of Wisconsin at Madison

Jason Cong, University of California at Los Angeles

John Costello, Altera

Andre' DeHon, Caltech

Rajeev Jayaraman, Xilinx

Tom Kean, Algotronix

Guy Lemieux, University of British Columbia

John Lockwood, Washington University

Wayne Luk, Imperial College

Malgorzata Marek-Sadowska, University of California at Santa Barbara

Majid Sarrafzadeh, University of California at Los Angeles

Eric Sather, Actel

Herman Schmit, Tabula

Satwant Singh, Lattice Semiconductor

Russell Tessier, University of Massachusetts at Amherst

John Wawrzynek, University of California at Berkeley

Steve Wilton, University of British Columbia