

FPGA 2004: Call for Papers

Twelfth ACM International Symposium on Field-Programmable Gate Arrays

Monterey, California
February 22-24, 2004

The ACM/SIGDA International Symposium on Field-Programmable Gate Arrays is the premier conference for presentation of advances in all areas related to FPGA technology. For FPGA 2004, we are soliciting submissions describing novel research and developments in the following (and related) areas of interest:

- ◆ **FPGA Architecture:** Combined FPGA fabric with system blocks (processors, etc.), Logic block & routing architectures, new commercial architectures, Field-Programmable Interconnect Chips and Devices (FPIC/FPID), revolutionary programmable and regular fabrics for ultra-deep submicron and nanotechnology fabrication.
- ◆ **CAD for FPGAs:** Placement, routing, logic optimization, technology mapping, system-level partitioning, logic generators, testing and verification, CAD for FPGA-based accelerators, CAD for incremental FPGA design.
- ◆ **Applications:** Innovative use of FPGAs, exploitation of FPGA features, novel circuits, high-performance and low-power/mission-critical applications, DSP techniques, uses of reconfiguration, FPGA-based cores.
- ◆ **FPGA-based and FPGA-like computing engines:** Compiled accelerators, reconfigurable computing, adaptive computing devices, systems and software.
- ◆ **Rapid-prototyping:** Fast prototyping for system-level design, Multi-Chip Modules (MCMs), logic emulation.

Authors are invited to submit English language PDF of their paper (12 pages maximum) and panel proposals by **September 26, 2003** by E-mail to fpga2004@ece.cmu.edu. Notification of acceptance will be sent by November 28, 2003. The authors of accepted papers will be required to submit the final camera-ready copy by December 5, 2003. A proceedings of the accepted papers will be published by ACM, and included in the Annual ACM/SIGDA CD-ROM Compendium publication.

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Please visit the web site < <http://fpga2004.ece.ubc.ca> > for more information.