FPGA 2001: Call for Papers
Ninth ACM International Symposium on
Field-Programmable Gate Arrays
Monterey, California
February 11-13 2001

The annual ACM/SIGDA International Symposium on Field-Programmable Gate Arrays is the premier conference for presentation of advances in all areas related to FPGA technology. For FPGA 2001, we are soliciting submissions describing novel research and developments in the following (and related) areas of interest:

♦ **FPGA Architecture:** Logic block & routing architectures, I/O structures and circuits, new commercial architectures, Field-Programmable Interconnect Chips and Devices (FPIC/FPID), Field-Programmable Analog Arrays (FPAA).

♦ **CAD for FPGAs:** Placement, routing, logic optimization, technology mapping, system-level partitioning, logic generators, testing and verification, CAD for FPGA-based accelerators.

♦ **Applications:** Innovative use of FPGAs, exploitation of FPGA features, novel circuits, high-performance and low-power/mission-critical applications, DSP techniques, uses of reconfiguration, FPGA-based cores.

♦ **FPGA-based computing engines:** Compiled accelerators, reconfigurable computing, adaptive computing devices, systems and software.

♦ **Rapid-prototyping:** Fast prototyping for system-level design, Multi-Chip Modules (MCMs), logic emulation.

Authors are invited to submit PDF of their paper (12 pages maximum) by **September 29, 2000** via E-mail to fpga2001@cse.ucsc.edu. Notification of acceptance will be sent by November 22, 2000. The authors of the accepted papers will be required to submit the final camera-ready copy by December 6, 2000. Proceedings of the accepted papers will be published by ACM, and included in the Annual ACM/SIGDA CD-ROM Compendium publication.

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Please visit the web site **<http://www.ecs.umass.edu/ece/fpga2001>** for more information.